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SERIAL NUMBER 10/612,849	FILING DATE 07/03/2003 RULE	CLASS 711	GROUP ART UNIT 2188	ATTORNEY DOCKET NO. 5646-42DVIP
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APPLICANTS

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** CONTINUING DATA *****

 This application is a CIP of 10/307,638 12/02/2002 PAT 6,754,777
 which is a DIV of 09/721,478 11/22/2000 PAT 6,546,461

GJP

** FOREIGN APPLICATIONS *****

NONE GJP

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

** 10/17/2003

Foreign Priority claimed <input type="checkbox"/> yes <input checked="" type="checkbox"/> no	STATE OR	SHEETS	TOTAL	INDEPENDENT
35 USC 119 (a-d) conditions met <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	COUNTRY	DRAWING	CLAIMS	CLAIMS
Verified and Acknowledged Examiner's Signature <i>[Signature]</i> Initials	CA	26	22	6

ADDRESS

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TITLE

FIFO memory devices having multi-port cache memory arrays therein that support hidden EDC latency and bus matching and methods of operating same

FILING FEE

 RECEIVED
 1038

 FEES: Authority has been given in Paper
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